

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

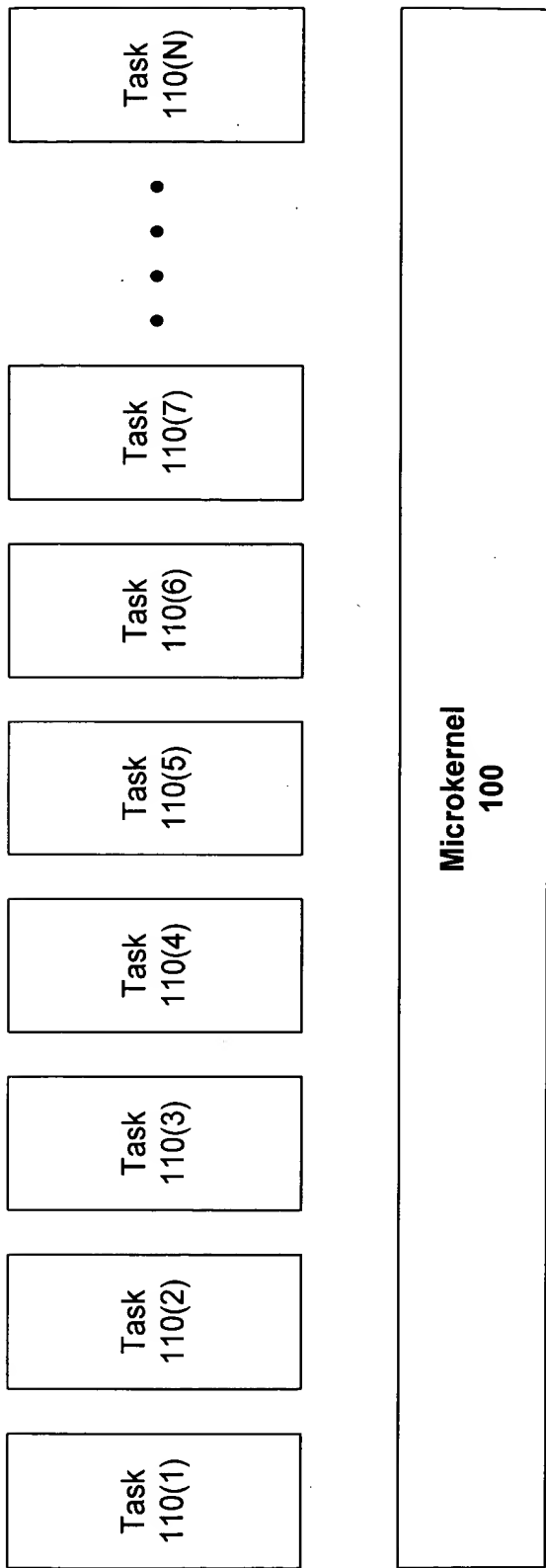


FIG. 1



APPROVED	O.G. FIG.
BY	CLASS/SUBCLASS
DEVT/ANAL	

Message  
300

I/O Channel Identifier 305	Operation Code 310
Result Field 315	
Argument Field 320	
Argument Field 325	
DDR 330	

FIG. 3



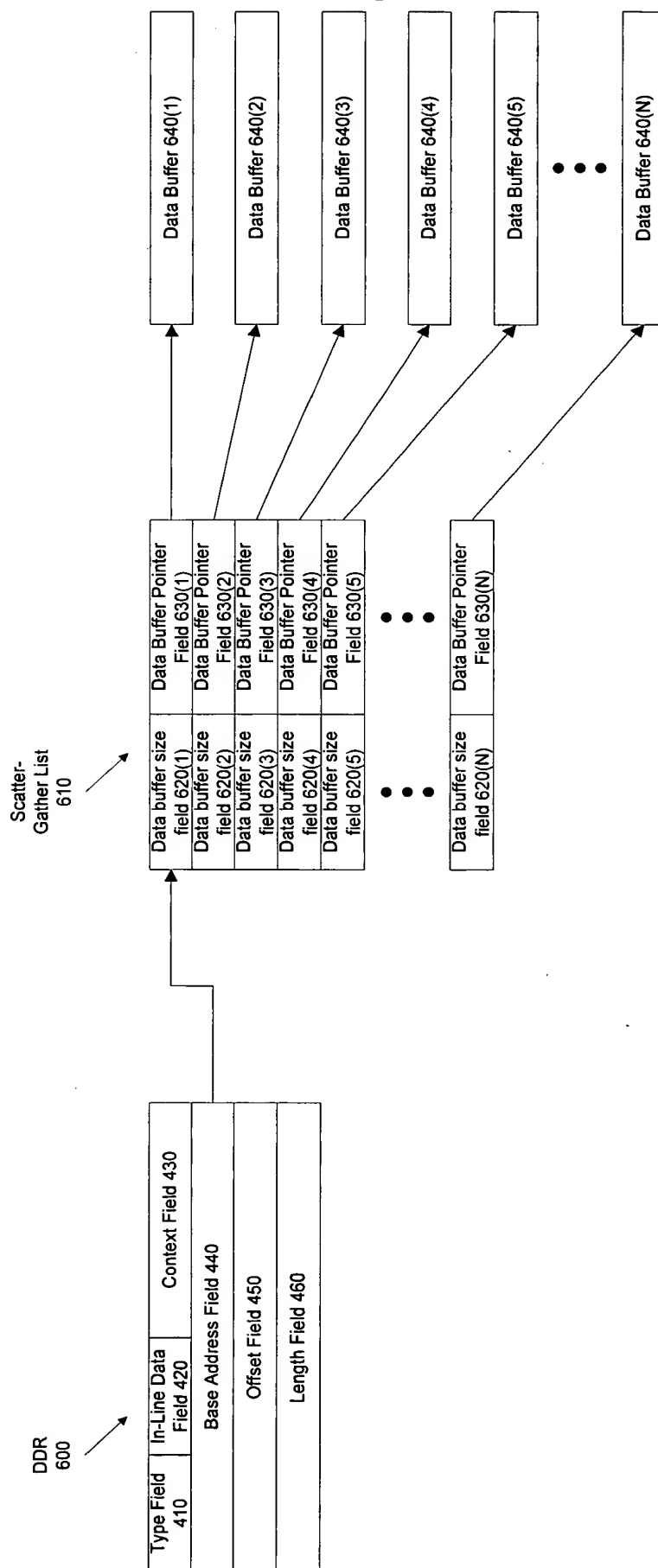
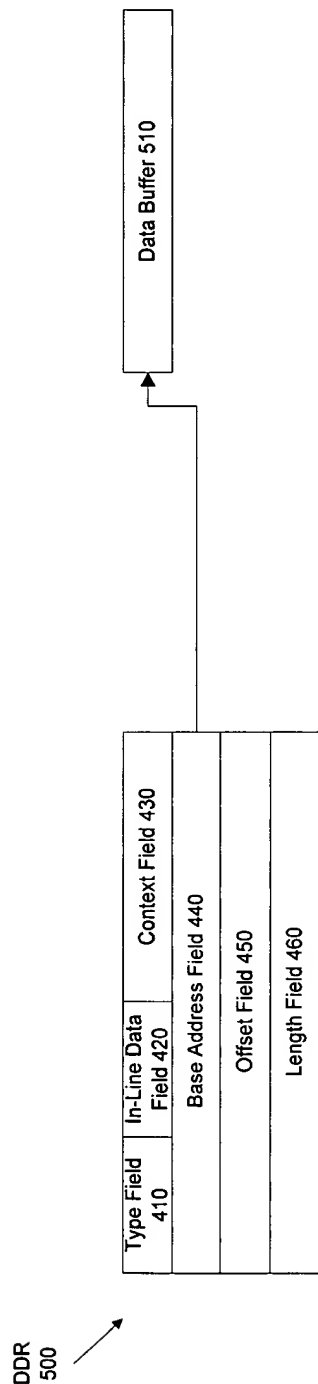
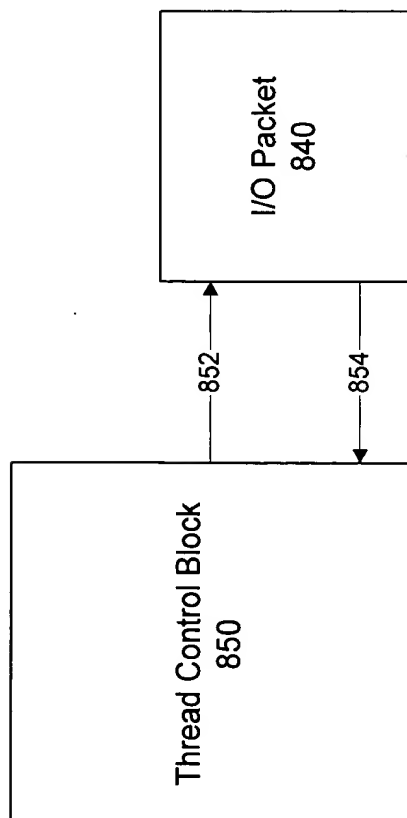




FIG. 7





The diagram illustrates a Thread Control Block (TCB) structure. On the left, a vertical rectangle is labeled "Thread Control Block" with the address "880" below it. To the right of this rectangle is a smaller, horizontally-oriented rectangle labeled "Message" with the address "870" below it. This represents the Message field within the TCB.

FIG. 8C



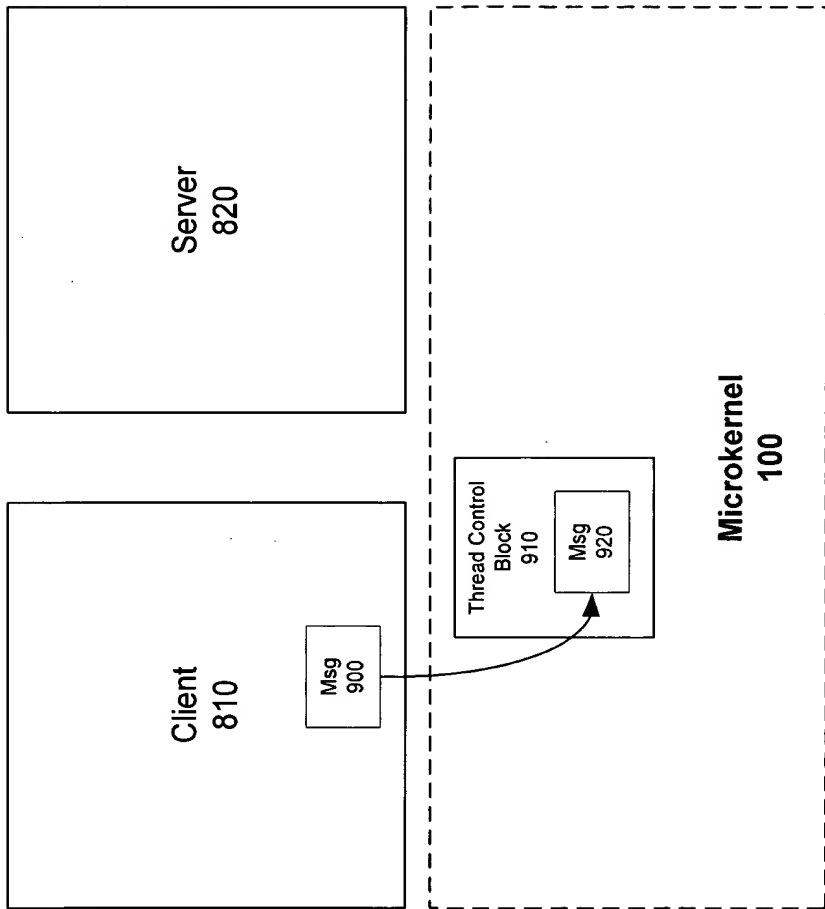


FIG. 9



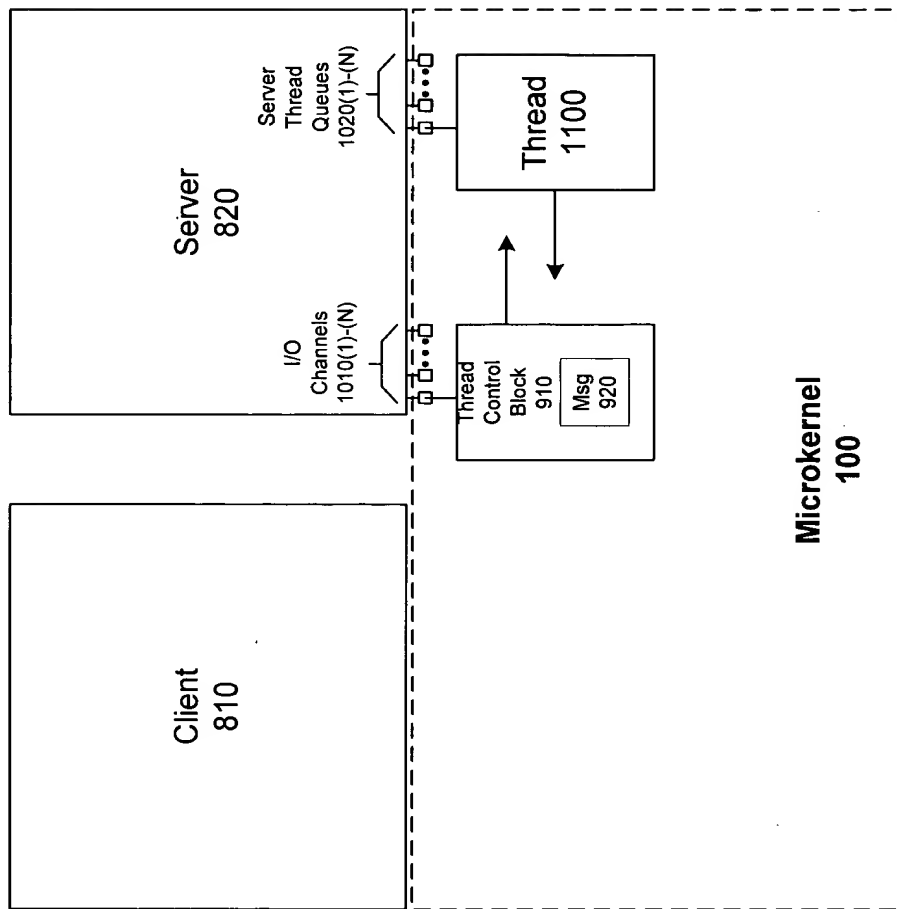


FIG. 11

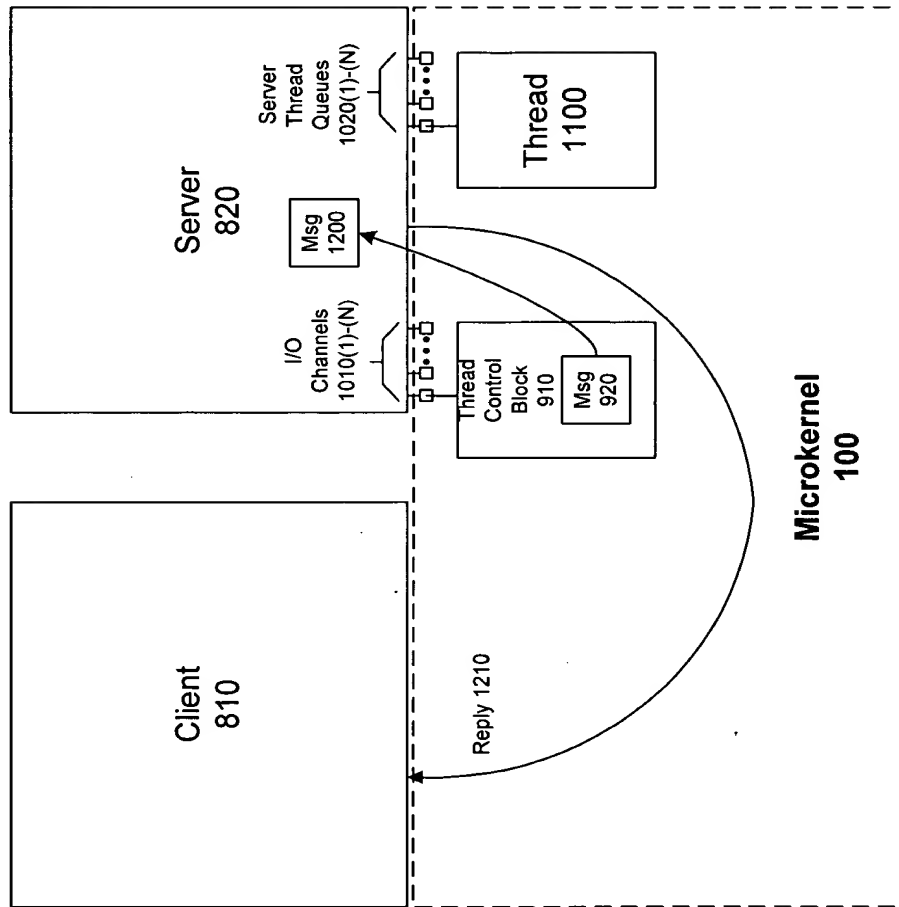


FIG. 12A

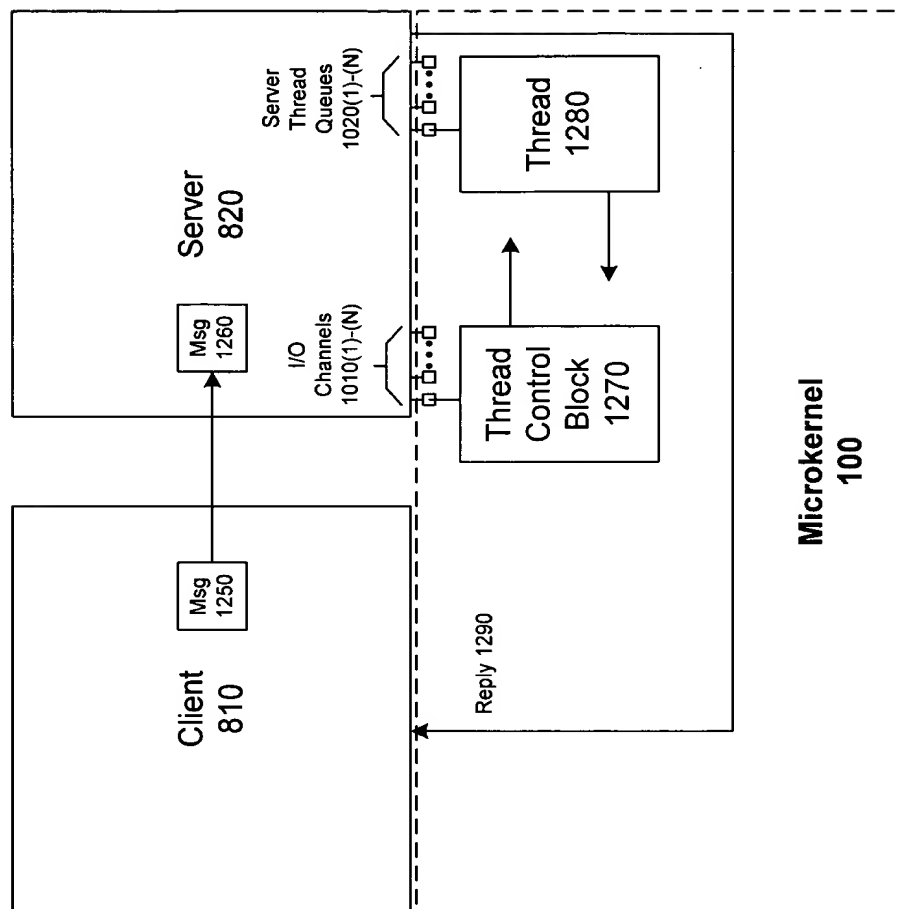


FIG. 12B

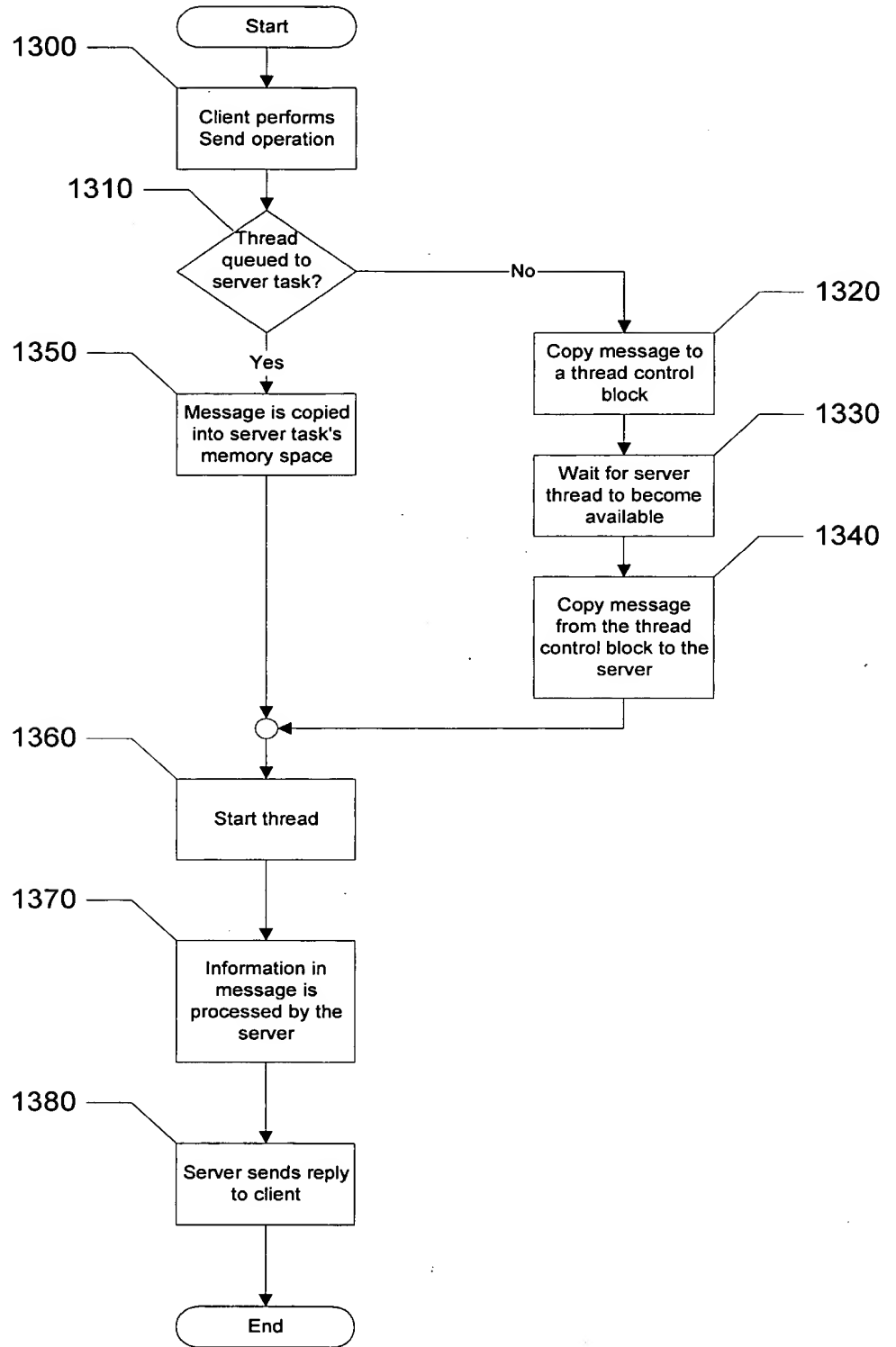


FIG. 13

FIG. 14A

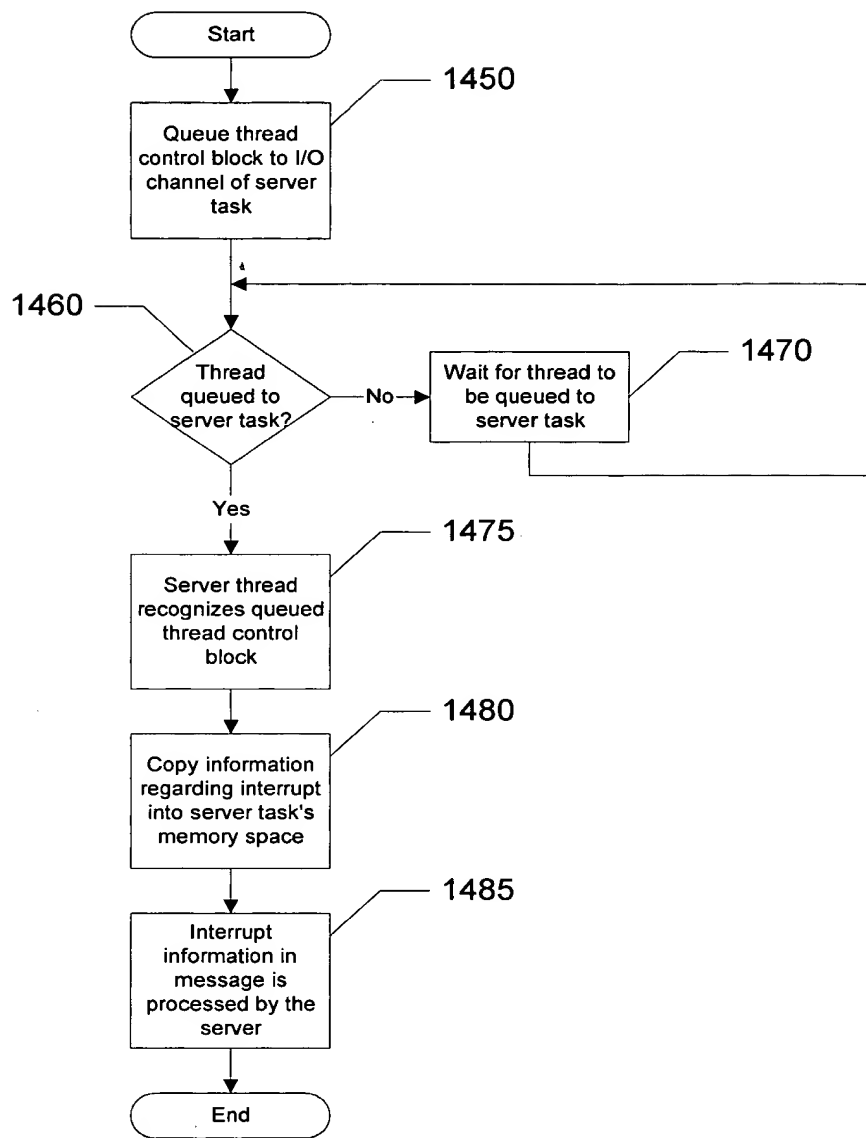


FIG. 14B



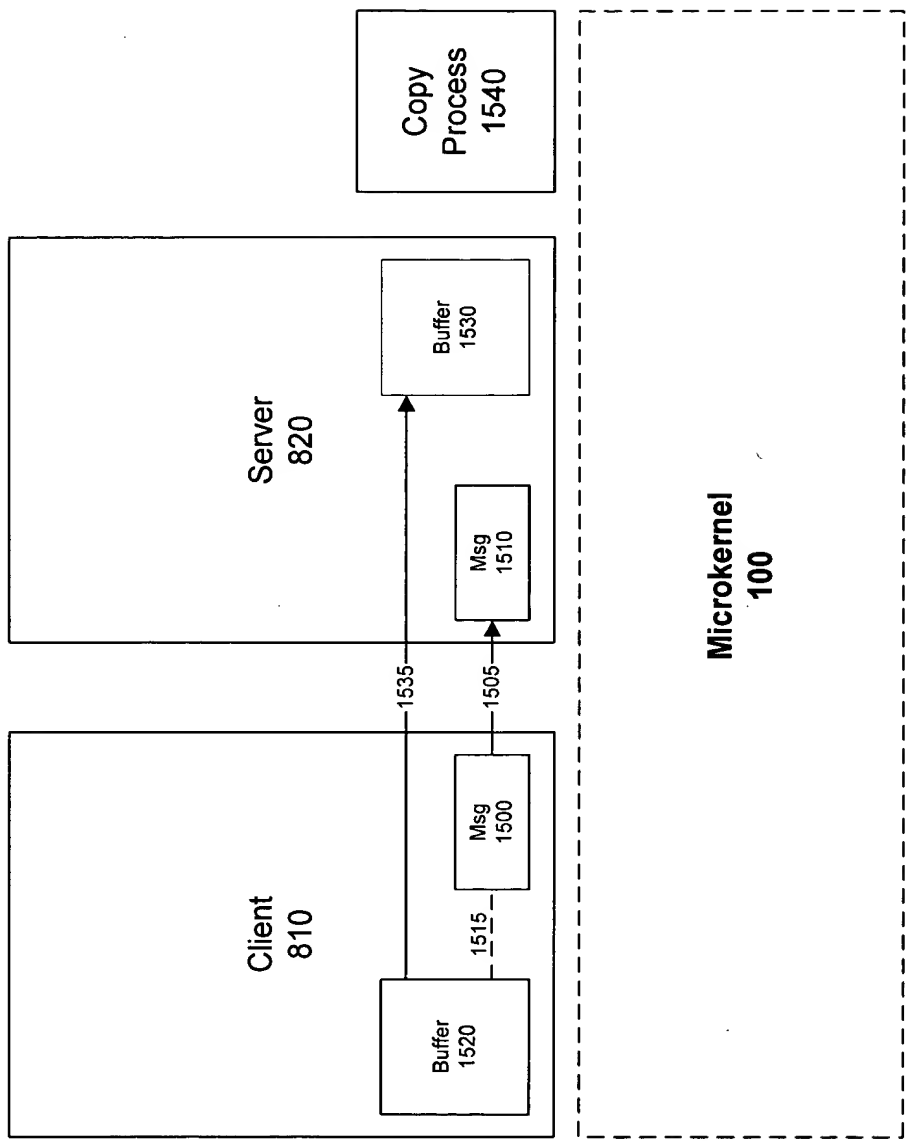


FIG. 15

000000-02000000

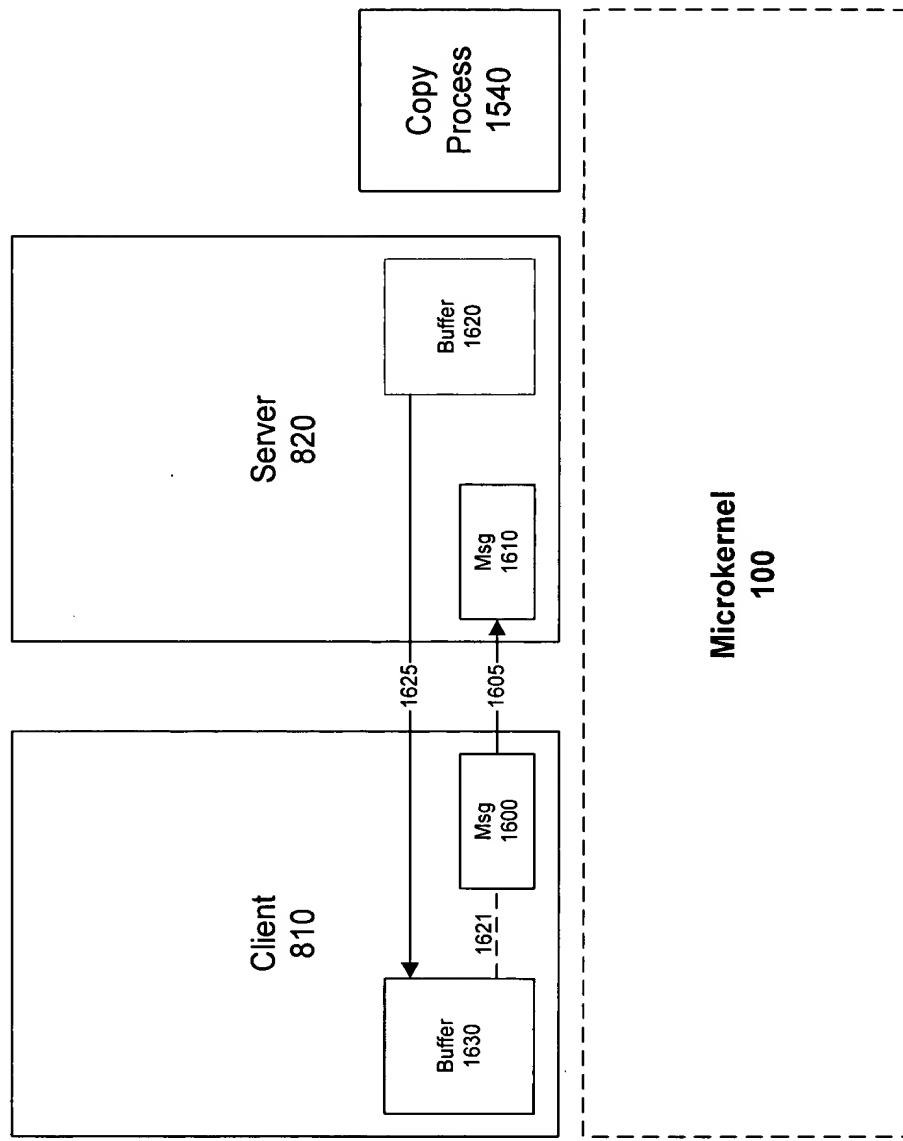


FIG. 16

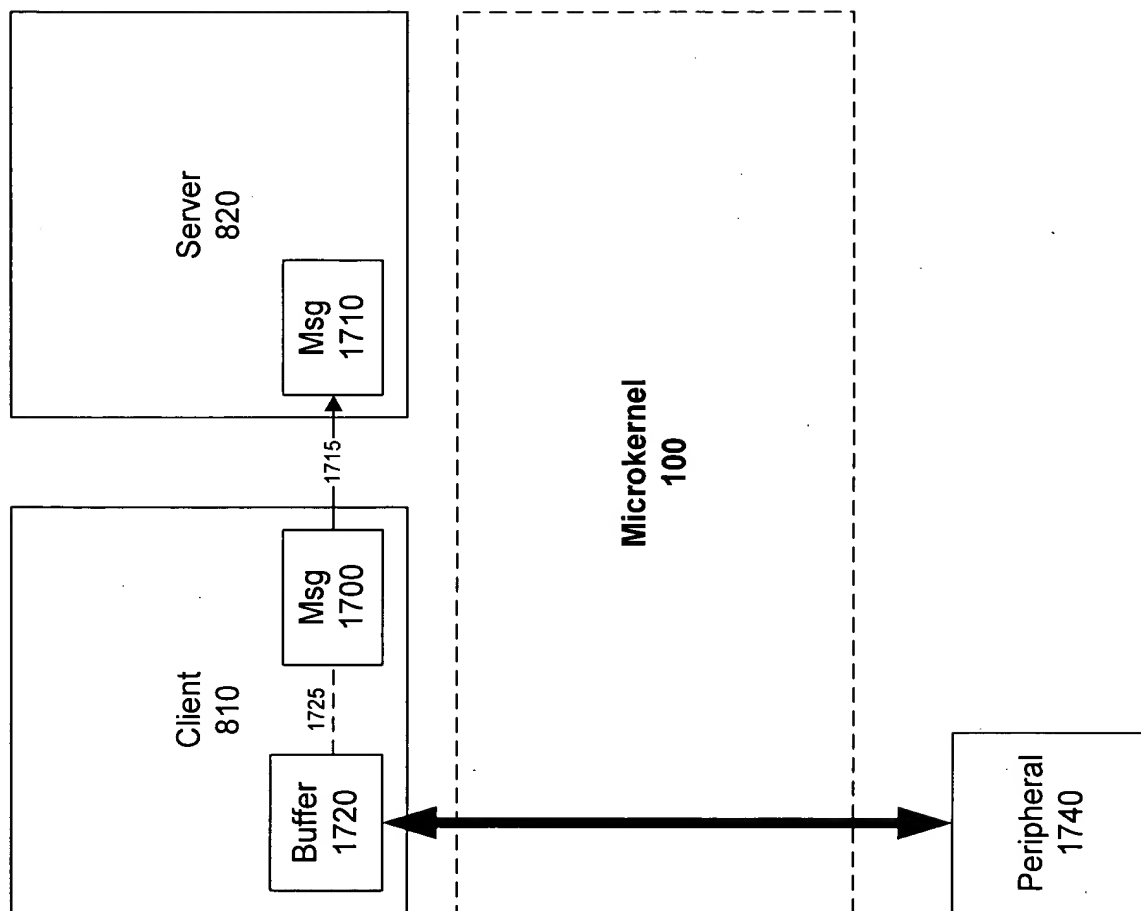


FIG. 17